

SPECIFICATIONS

CIO-DAC08

CIO-DAC16

Analog Outputs



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Power Consumption

CIO-DAC16	
+5V supply	435 mA typical, 525 mA max
+12V supply	140 mA typical, 180 mA max
–12V supply	80 mA typical, 105 mA max
CIO-DAC08	
+5V supply	435 mA typical, 525 mA max
+12V supply	75 mA typical, 98 mA max
–12V supply	52 mA typical, 68 mA max

Analog Output

D/A type	AD7237
Resolution	12 bits
Number of channels	
CIO-DAC16	16 Voltage Outputs
CIO-DAC08	8 Voltage Outputs
Output Ranges	±10V, ±5V, ±2.5V, 0 to 10V, 0 to 5V, 0 to 2.5V. Each channel independently switch-selectable.
D/A pacing	Software paced
Data transfer	Software
Offset error	Adjustable to zero
Gain error	Adjustable to zero
Differential non-linearity	±½ LSB max
Integral non-linearity	±½ LSB max
Monotonicity	12 bits
Gain drift (DAC)	±30 ppm/°C max
Offset drift (DAC)	±3 ppm/°C max
Throughput	System-dependent
Slew Rate	0.3 V/μs Typical
Settling time (20V step to .01%)	70 μs
Current Drive	±5 mA min
Output short-circuit duration	Indefinite
Output coupling	DC
Output resistance (OP-07)	0.1 ohm max
Miscellaneous	Double-buffered output latches Update DACs individually or simultaneously (jumper-selectable by pairs) DAC output state on power up and reset undefined

Environmental

Operating temperature range	0 to 70°C
Storage temperature range	–40 to 100°C
Humidity	0 to 90% non-condensing

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