SPECIFICATIONS

CIO-DAC08-I
CIO-DAC16-I

Analog Current Outputs

Revision 3, October, 2000

Power Consumption

CIO-DAC16-I	
+5V supply	320 mA typical, 500 mA max
+12V supply	150 mA typical, 190 mA max
–12V supply	100 mA typical, 130 mA max
CIO-DAC08-I	
+5V supply	320 mA typical, 500 mA max
+12V supply	75 mA typical, 100 mA max
–12V supply	50 mA typical, 65 mA max

Analog Output

D/A type	AD7237
Resolution	12 bits
Number of channels	
CIO-DAC16-I	16 Current Outputs
CIO-DAC08-I	8 Current Outputs
Output Range	4 to 20mA
Voltage compliance	6 to 36V
D/A pacing	Software paced
Data transfer	Software
Offset error	Adjustable to zero
Gain error	Adjustable to zero
Differential nonlinearity	±1 LSB max
Integral nonlinearity	±1 LSB max
Monotonicity	Guaranteed monotonic to 15 bits over temperature
Gain drift (DAC)	±15 ppm/°C max
Bipolar offset drift (DAC)	±5 ppm/°C max
Unipolar offset drift (DAC)	±3 ppm/°C max
Throughput	System dependant
Settling time (Full scale step to .01%)	12 µs typ, 19 µs max
Miscellaneous	Double buffered output latches
	DAC output state on power up and reset undefined

Environmental

Operating temerature range	0 to 70°C
Storage temerature range	–40 to 100°C
Humidity	0 to 90% non-condensing

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